

11002 U.S. PTO
10/080747
03/23/03

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10080747	02/22/2002	347	2853	2853	00

**APPLICANTS: Buswell Shen; Templin Paul; Jenssen Conrad; MacKenzie Mark;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 09/872,775 06/01/2001 } OK

** FOREIGN APPLICATIONS VERIFIED: None

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	10001915-3
Verified and Acknowledged Examiners's initials		
TITLE : Slotted semiconductor substrate having microelectronics integrated thereon		

U.S. DEPT. OF COMMERCE/PAT. & TM-PTO-435L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
ISSUE FEE		Print Fig.	
Amount Due	Date Paid		
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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